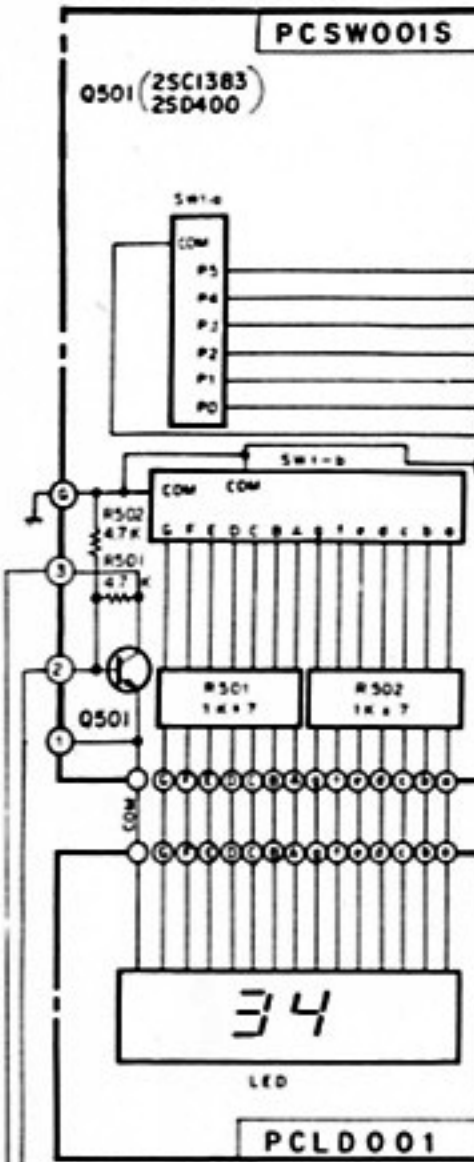
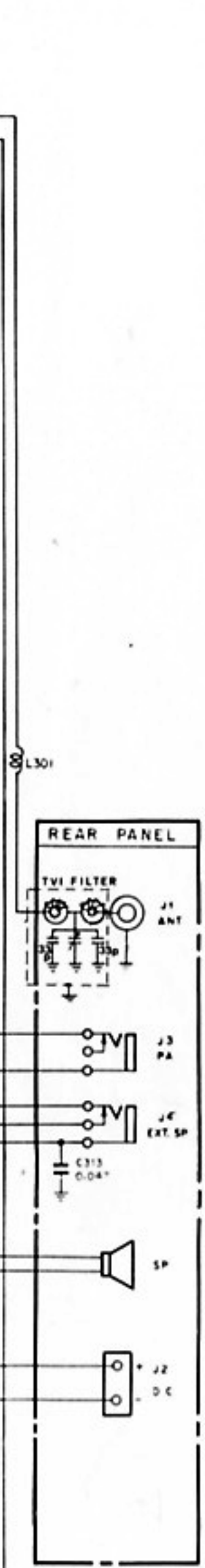
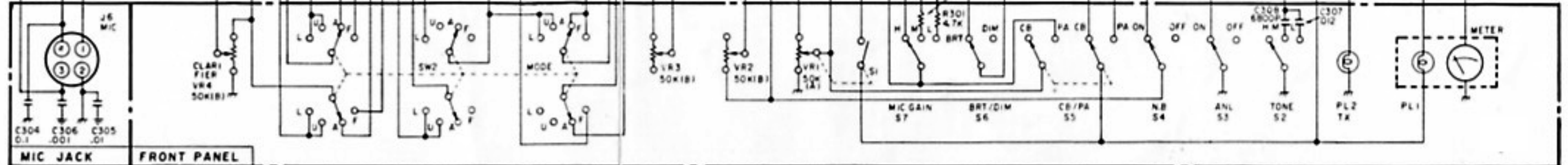
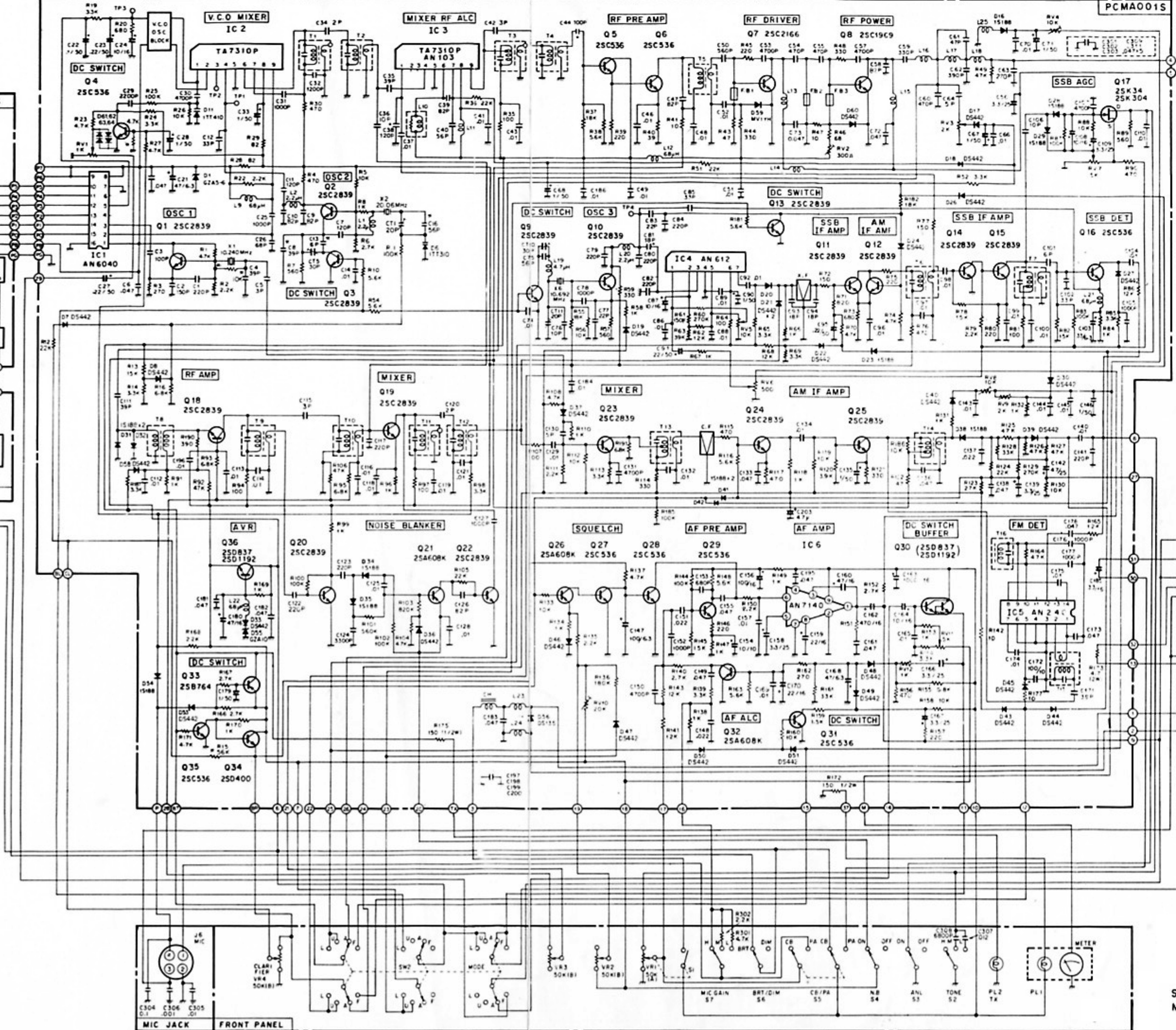
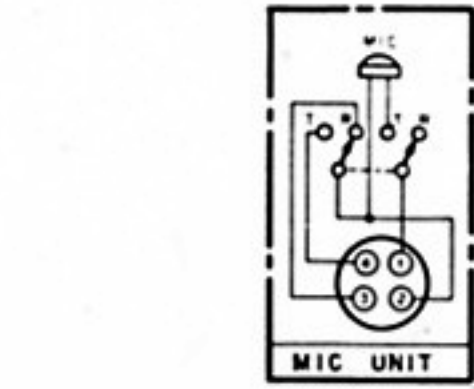
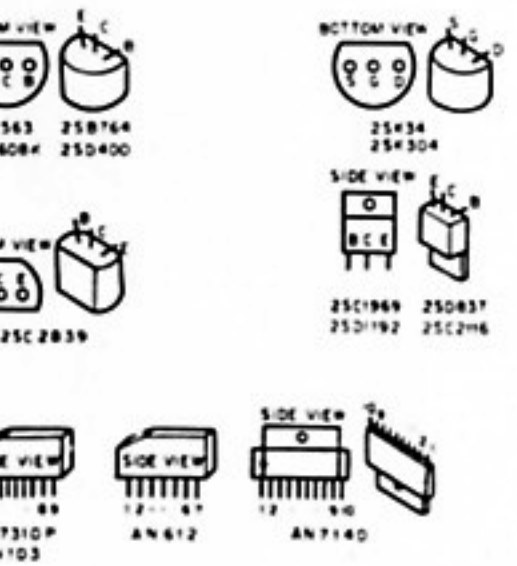


NOTE:
 ALL VOLTAGES MEASURED FROM PC BOARD
 GROUND WITH DC VTVM AT NO SIGNAL
 (AT 13.8V POWER SUPPLY) IF MEASUREMENT
 VALUES OBTAINED ARE IN EXCESS OF ±20%
 OF VALUES SHOWN THEN REASON FOR
 DIFFERENCE SHOULD BE CORRECTED.
 Δ TX: 0.15B: () AM TX
 ↓ CHASSIS GND PC BOARD GND
 * ADJUSTED (TYPICAL VALUE SHOWN)



TRANSISTOR BASE INFORMATION
 F EMITTER S SOURCE
 C COLLECTOR G GATE
 B BASE D DRAIN



SCHEMATIC DIAGRAM
 MODEL ALAN 885